

Tuesday 16 February 2016

Time	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00	17:00-18:00
Hall A	Workshop 1: Texas Instruments C2000 DSP Code Developments Using MATLAB/Simulink and Code Composer Studio v5					Workshop 3: FPGA Programming Using MATLAB/Simulink				
Hall B		Workshop 2: EMC of Power Electronics and Drives			Lunch		Workshop 2: EMC of Power Electronics and Drives			
Hall C						Workshop 4: Application of power electronics in the railway industry				

Wednesday 17 February 2016

Time	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00	17:00-18:00
Hall A	Reg.	Opening Ceremony	Keynote Speech 1	Keynote Speech 2	Lunch		Session A1		Break	Session A2
Hall B							Session B1			Session B2
Hall C							Session C1			Session C2
							Poster 1			Poster 2

Thursday 18 February 2016

Time	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00	17:00-18:00
Hall A	Session A3		Break	Keynote Speech 3	Keynote Speech 4	Lunch		Session A4	Panel	Closing Ceremony
Hall B	Session B3							Session B4		
Hall C	Session C3							Session C4		
	Poster 3							Poster 4		